
Reset ML-1660 1665 1666 V1.01.00.34f (generator 1660 V34).19

Another thing that would be close to the 6800 is the debouncing of reset. The table shows the three different thresholds and 4 combinations of debouncing with rising and/or falling of the reset. Only the first two are shown here. Also note, there is a more detailed table in the 6800 datasheet in section 4.6.6. I have to spend more time figuring out the exact timing of the reset. It is possible that a slightly different reset signal is sent depending on the load resistors on the output. But note that the MCP130 already has an internal resistor (about 5K). Quick guestimate would put the high level output load (current supplied by that 5K resistor) would be in the 10-20uA or about a 0.05 to 0.1V drop. Anything lower than (5-0.2) or 4.8V and I would suspect something else on reset line. However, the 6800 datasheet still says anything over 2V should be a valid release of the reset line. If you mean to use the 1K resistor to pull the output up then this 1K resistor (when combined with internal 5K) will result in a ~6mA load to the reset generator when it is driving the output low. A tad high but you should still be fine... I would go with about a 2.2K resistor. They are too close, you have to go back and check the manual for more details here. I don't remember any details but this gets a lot of things checked that doesn't appear to be necessary here... The bit is probably there to make the reset signal work before the chip sees the data. Synplify doesn't actually know anything about the reset line and tries to do something that does not make sense. This is a discussion from between 2000 and 2004. and the problem was exactly like this. This should not happen since we have a signal completely above the part's positive supply that is fully terminated with a pullup resistor of at least 10K (no need to bother with the resistors, need to see 'How to use a Pullup' on BTH to find the bigger picture).



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