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Elsawin 3.9 Audi VAG etka 4.2.3 VAG etka 4.2.3 Download VAG etka 4.2.3 Version Elsawin v4.1 Audi DW Audi AUDI Skoda AUDI VW Audi VW Audi VW Audi VW Audi VW DW AudiThe present invention relates to the field of integrated circuits. Specifically, the invention relates to a method for identifying a defect in an integrated circuit. Integrated circuits are found in a variety of common devices, such as computers, cell phones, portable music players, and printers to name a few. The demand for greater functionality

and smaller devices has forced integrated circuit manufacturers to reduce the size of their integrated circuits while maintaining, or even improving, the integrated circuit's reliability. Integrated circuits can fail for a variety of reasons. These may include inadequate wafer yields (in which the integrated circuits fail before they can be completely tested), manufacturing defects, and failure of the chip after it is manufactured. Some defects may cause harm to a system user immediately. For example, when a static random access memory (SRAM) memory cell is defective, it may cause immediate system failure (e.g., a memory crash). Other defects may not cause harm to the system user immediately and will only cause problems when the defective integrated circuits are operated in a system. For example, when a microprocessor's memory cells fail, the microprocessor may or may not crash immediately depending on how much memory is installed in the system.

Therefore, defect testing on integrated circuits is very important to the manufacturer to ensure that the chips they manufacture will perform as expected in their final product. Although there are various methods for testing integrated circuits, defect identification and analysis in post-manufacturing testing (PMT) is very time consuming and complex. For instance, a problem which frequently occurs in PMT is the identification of sub-system failures in a complex system. Therefore, a system is desired which addresses the problem of reducing time and complexity in defect identification during post-manufacturing testing. in which the cell is in a high threshold state and the cell is in a low threshold state. The memory is normally either off (a logical 0) or on (a logical 1). The switching mechanism used to program the memory is called a program resistor. The program resistor is connected to one side of the cell and can be turned on and off to program the cell into a threshold state

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